

USB 2.0 Micro SD CATT
host port card slot Expansion port



CATT pinout

14 +3.3v output	12 GND	10 GPIO 3	8 GPIO 5	6 GPIO 34	4 I2C SCL GPIO 13	2 GND
13 P4 reset input	11 GND	9 GPIO 2	7 GPIO 4	5 GPIO 15	3 I2C SDA GPIO 12	1 +3.3v output

SAO pinout

				6 IO 2 GPIO 34	4 ISCL GPIO 13	2 GND
				5 IO 1 GPIO 15	3 I2C SDA GPIO 12	1 +3.3v output

Upside down, be careful the bits don't fall out

PMOD pinout

"6" +3.3v	"5" GND	"4" DATA 3 GPIO 3	"3" DATA 2 GPIO 5	"2" DATA 1 GPIO 34	"1" DATA 0 GPIO 13	
"12" (+3.3v)	"11" GND	"10" DATA 7 GPIO 2	"9" DATA 6 GPIO 4	"8" DATA 5 GPIO 15	"7" DATA 4 GPIO 12	

JTAG pinout

14 VREF	12 GND	10 MTDI	8 MTDO	6 DETECT	4	2 GND
13 RESET	11 GND	9 MTCK	7 MTMS	5	3	1 VREF

JTAG is enabled by pulling DETECT (6) to GND while powering on the device.
Note that software can overrule JTAG mode by using any of the required pins, causing the debugger to disconnect.